



2015 移动开发者大会

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# Linux驱动的架构思考

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## 约30种体系架构

```
bs14@shaunxand02:~/atlas7-android/kernel/arch$ ls
Kconfig      c6x          m68k         parisc       um
alpha        cris         metag        powerpc      unicore32
arc          frv          microblaze   s390         x86
arm          h8300       mips         score        xtensa
arm64        hexagon     mn10300     sh
avr32        ia64        nios2        sparc
blackfin     m32r        openrisc     tile
```

## 约130种驱动

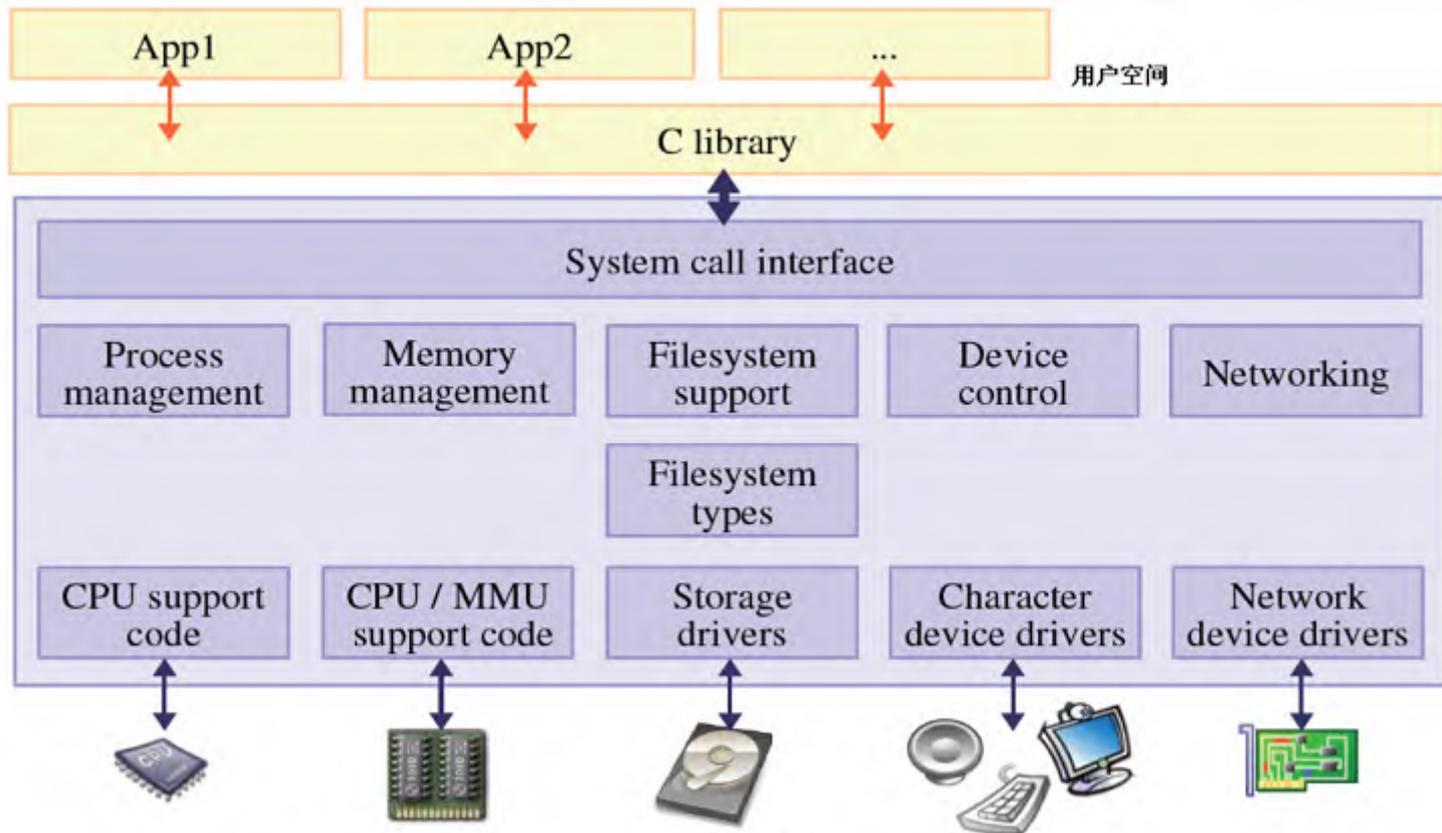
```
bs14@shaunxand02:~/atlas7-android/kernel/drivers$ ls
Kconfig          connector        hsi              mailbox          oprofile        regulator        thermal
Makefile         cpufreq         hv               mcb              parisc          remoteproc      thunderbolt
accessibility    cpuidle         hwmon            md               parport         reset           tty
acpi             crypto          hwspinlock      media            pci              rpmsg           uio
amba            dca             hwtracing       memory           pcmcia          rtc             usb
android          devfreq         i2c              memstick         perf            s390            uwb
ata              dio             ide              message          phy              sbus            vfio
atm              dma             idle             mfd              pinctrl         scsi            vhost
auxdisplay       dma-buf         iio              misc             platform        sfi             video
base             edac            infiniband      mmc              pnp              sh              virt
bcma             eisa            input            mtd              power            sn              virtio
block            extcon          iommu            net              powercap        soc             vlynq
bluetooth        firewire        ipack            nfc              pps              spi             vme
bus              firmware        irqchip          ntb              ps3              spmi            wl
cdrom            fmc             isdn             nubus            ptp              ssb             watchdog
char             gpio            leds             nvdim            pwm              staging         xen
clk              gpu             lguest          nvmem            rapidio          target          zorro
clocksource      hid             macintosh        of               ras              tc
```

- 这么多CPU，这么多外设。如果BSP不好移植，Linux不可能在嵌入式如此成功；
- 那么目标是什么？

最小化移植Linux到一个新的体系架构、SoC的工作；  
内核核心部分（调度器、内存管理、设备访问等）  
应该通用；

外设、IP驱动应该跨平台；

驱动核心层应该尽可能做更多的通用工作。



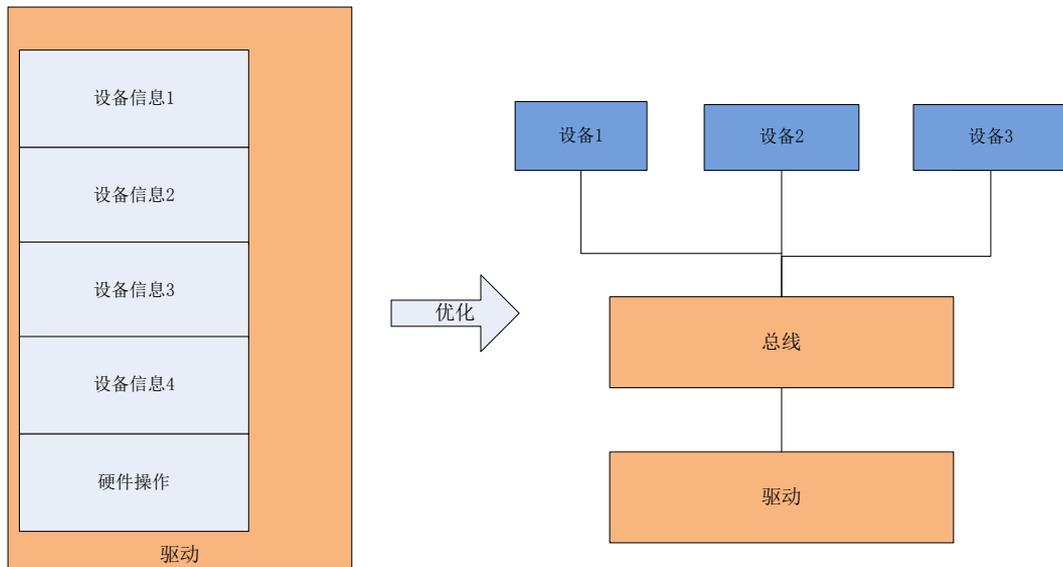
- kernel/arch/c6x\$ find ./ -name "\*.c|h|S" | xargs wc -l -- | grep total  
10345 total
- kernel/arch/hexagon\$ find ./ -name "\*.c|h|S" | xargs wc -l -- | grep total  
12045 total
- kernel/arch/openrisc\$ find ./ -name "\*.c|h|S" | xargs wc -l -- | grep total  
9433 total

mm, ipc, kernel, fs, net等是通用的，  
新的处理器架构需要对其底层进行  
支持。

2011-11-01	Hexagon: Add hypervisor interface
2011-11-01	Hexagon: Add memcpy and memset accelerated functions
2011-11-01	Hexagon: Add checksum functions
2011-11-01	Hexagon: Add delay functions
2011-11-01	Hexagon: Add threadinfo
2011-11-01	Hexagon: Add processor and system headers
2011-11-01	Hexagon: Add syscalls
2011-11-01	Hexagon: Add atomic ops support
2011-11-01	Hexagon: Add bitops support
2011-11-01	Hexagon: Core arch-specific header files
2011-11-01	Hexagon: Add generic headers

2011-11-01	Hexagon: Add basic stacktrace functionality for Hexagon architecture.
2011-11-01	Hexagon: Add configuration and makefiles for the Hexagon architecture.
2011-11-01	Hexagon: Comet platform support
2011-11-01	Hexagon: kgdb support files
2011-11-01	Hexagon: Add page-fault support.
2011-11-01	Hexagon: Add page table header files & etc.
2011-11-01	Hexagon: Add ioremap support
2011-11-01	Hexagon: Provide DMA implementation
2011-11-01	Hexagon: Implement basic TLB management routines for Hexagon.
2011-11-01	Hexagon: Implement basic cache-flush support
2011-11-01	Hexagon: Provide basic implementation and/or stubs for I/O routines.
2011-11-01	Hexagon: Add user access functions
2011-11-01	Hexagon: Add locking types and functions
2011-11-01	Hexagon: Add SMP support
2011-11-01	Hexagon: Provide basic debugging and system trap support.
2011-11-01	Hexagon: Add ptrace support
2011-11-01	Hexagon: Add time and timer functions
2011-11-01	Hexagon: Add interrupts
2011-11-01	Hexagon: Add startup code
2011-11-01	Hexagon: Add init_task and process functions
2011-11-01	Hexagon: Add signal functions
2011-11-01	Hexagon: Support dynamic module loading.
2011-11-01	Hexagon: Export ksyms defined in assembly files.

驱动只管驱动，设备只管设备，总线则负责匹配设备和驱动，而驱动则以标准途径拿到板级信息，这样，驱动就可以放之四海而皆准了



- kernel/arch/arm\$ git reset --hard v2.6.39

```
find ./ -name "*.c|h|S" | xargs wc -l -- | grep total
```

**699157** total

大量平台code :

```
static struct resource i2c_resource1[] = {
    [0] = {
        .start = INT_I2C,
        .end   = INT_I2C,
        .flags = IORESOURCE_IRQ,
    },
    [1] = {
        .start = TEGRA_I2C_BASE,
        .end   = TEGRA_I2C_BASE + TEGRA_I2C_SIZE-1,
        .flags = IORESOURCE_MEM,
    },
};
```

```
struct platform_device tegra_i2c_device1 = {
    .name      = "tegra-i2c",
    .id        = 0,
    .resource  = i2c_resource1,
    .num_resources = ARRAY_SIZE(i2c_resource1),
    .dev = {
        .platform_data = 0,
    },
};
```

- kernel/arch/arm\$ git reset --hard v3.14

```
find ./ -name "*.[c|h|S]" | xargs wc -l -- | grep total
```

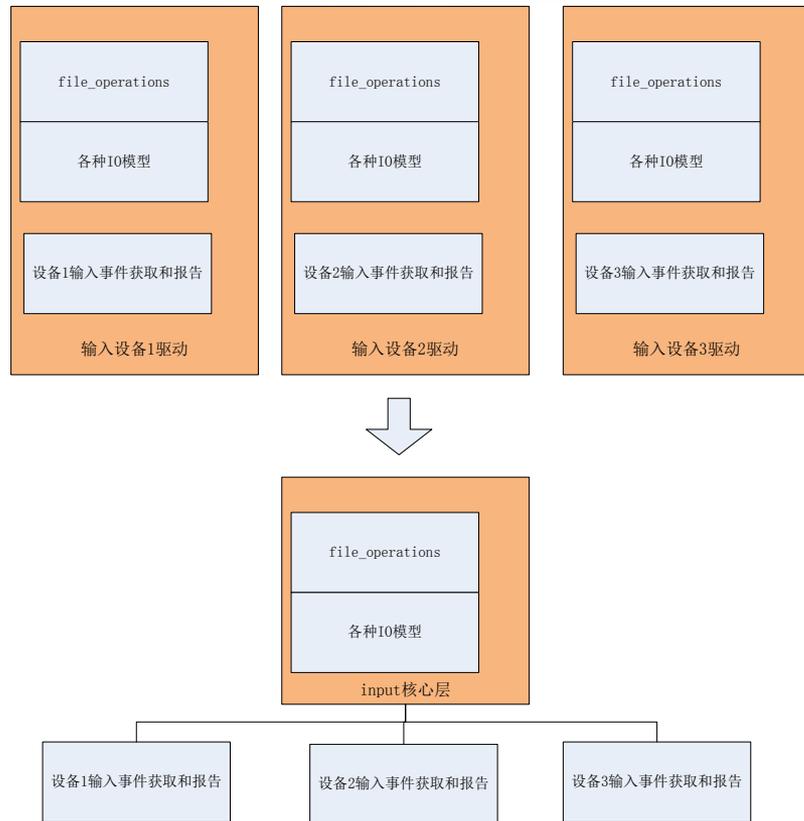
**592959** total

- 但是3.14支持的SoC远大于2.6.39, 以dts描述硬件信息

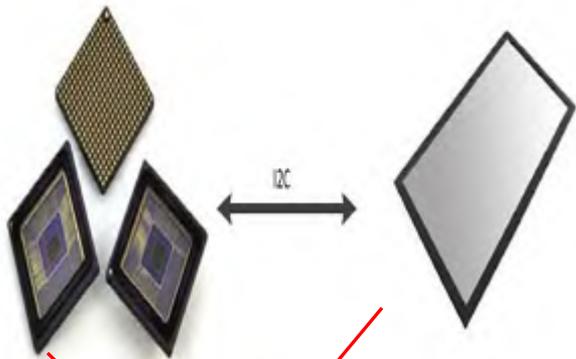
```
gic: interrupt-controller@10301000 {  
    compatible = "arm,cortex-a9-gic";  
    interrupt-controller;  
    #interrupt-cells = <3>;  
    reg = <0x10301000 0x1000>,  
        <0x10302000 0x0100>;  
};
```

- arch/arm/mach-xxx的一些timer , gpio , pinmux , clock等变为drivers/

将软件进行分层设计：比如提炼一个input的核心层出来，把跟Linux接口的事情以及整个一套input事件的buffer机制都在这里实现掉，底层只管中断和报事件

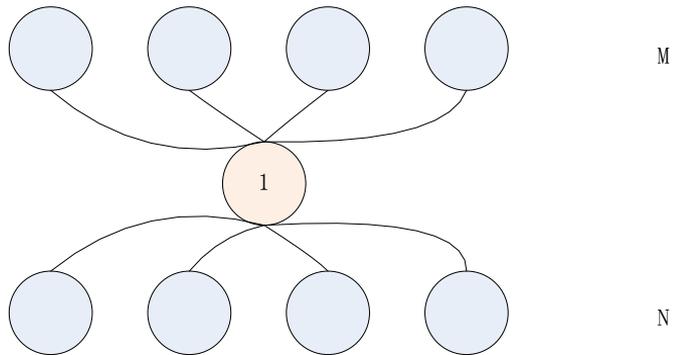
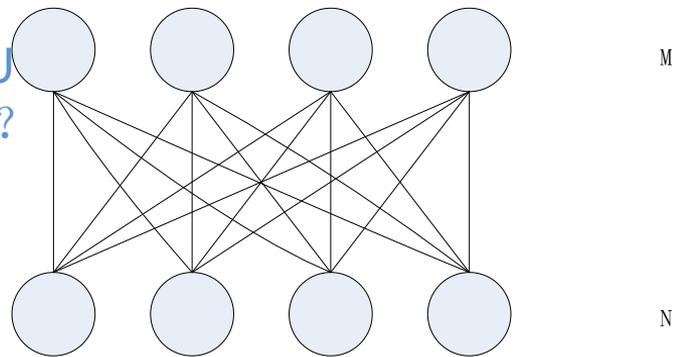


# 总线上接了一个外设，怎么去耦合？

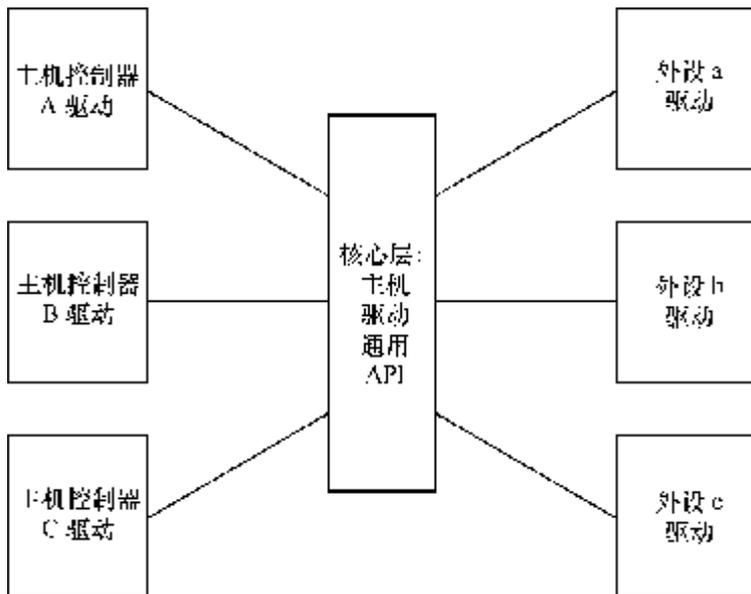


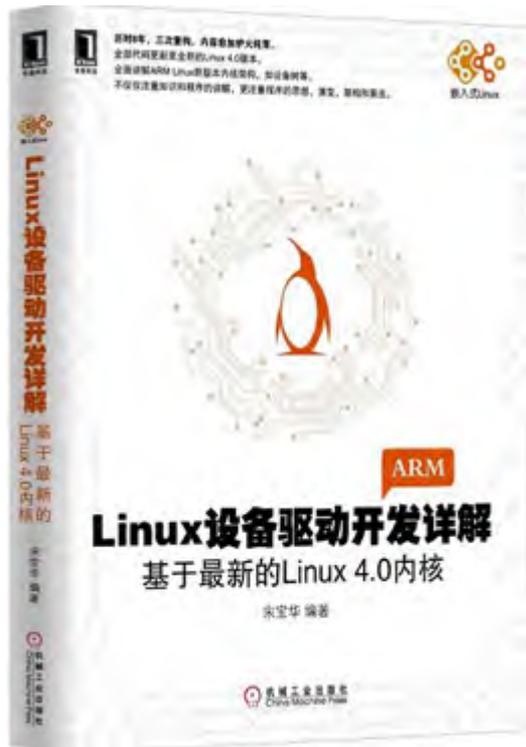
~~cpu\_xxx\_i2c\_reg\_write()  
cpu\_xxx\_i2c\_reg\_read()  
tp\_yyy\_reg1\_handle()  
cpu\_xxx\_i2c\_reg\_write()  
cpu\_xxx\_i2c\_reg\_read()  
tp\_yyy\_reg2\_handle()  
...~~

假设M个 CPU  
接N个触摸屏？



- 外设只是访问核心层的通用的API进行数据传输，主机和外设之间可以进行任意的组合







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